

Using STEP file capability in PCB Editor

STEP Model Support in PCB Editor

Overview

All versions of PCB Editor (OrCAD X and Allegro X) allow users to see their board design in a 3DX canvas. This methodology uses STEP models that are mapped either directly in the library (associated with the filename.dra) or assigned directly in the board file (filename.brd) itself. You can also define mechanical components (covers, brackets, enclosures) and assign step models to these parts in the board.

This note will cover:

- Map PCB Editor symbols to STEP models
- Use new STEP model features in the 3DX Canvas
- Associate enclosure STEP models for 3D Viewing
- Exporting a PCB Editor board as a STEP model

STEP Models

STEP models are XML formatted files that describe graphical detail for a physical part. There are currently three STEP model formats that support most electrical and mechanical parts used on a PCB.

- AP203 Aerospace's STEP AP203 Configuration controlled 3D design.
- AP214 Automotive's STEP AP214 Core data for automotive mechanical design processes.
- AP242 Merging of 2 ISO standards, AP203 and AP214. AP242 is designed modular, with regular increments in order to bring added value to users. A first edition (AP242E1) brings a first set of new features (overall for PMI, tessellation, external element references). A second edition (AP242E2) is in preparation to complete the case coverage.

Obtaining STEP models is possible through manufacturer sites, but not all manufacturers provide STEP models. There are several 3rd part library solutions that, for a subscription or fee, will create and distribute STEP models. Some MCAD tools provide the capability to export STEP models of their libraries and their user communities provide a shared library of STEP models readily available to users. A user may search throughout the internet to locate a site that best works for them. A recommended free site (although you must register) for 3D Step Models is: - <u>http://www.3dcontentcentral.com/Default.aspx</u>

See the following web sites more information on STEP models:

http://www.prostep.org/nc/en.html

http://www.prostep.org/en/projects/step-ap-242.html

STEP Model Viewers

There are several free or trial versions of 3D STEP model viewers available by searching the World Wide Web. If permissible, locate and install one for use in viewing a PCB Editor board STEP model.

© 2025 Parallel Systems Limited

symbol MUST contain a PACKAGE GEOMETRY PLACE BOUND TOP or PLACE BOUND BOTTOM element.

Symbol to STEP Model Mapping

to position the STEP model correctly to the symbol model.

in the 3D canvas. The mapping data created is then instantiated into the symbol as a property.

STEP models are associated to PCB Editor package and mechanical symbols through a mapping tool. This mapping tool sets the STEP model name to the symbol and defines offset information to correctly position the STEP model

STEP models, as of this writing, have no standardized origin or orientation more especially in relationships to 2D models. Most STEP model origins generally appear to have an origin located at the absolute center of the model, but this can never be guaranteed. Orientation will also vary from model to model. Correct positioning of the STEP model may require adjustment in rotation about the X, Y, and Z axis. Proper positioning of the 3D model may also require adjustments in the X, Y, and Z axis. The STEP model mapping tool assists the user in defining these offsets by visually comparing the PCB Editor symbol to the STEP model, where the user adjusts offset and rotation values

Important: For a mapped or unmapped model to be displayed in the PCB Editor 3DX canvas, the board or package

Direlay	Preference	Value	Effective	Favorite
	devpath		Command	
	interfacepath		Command	
File_management	miscpath		Command	
Ic_packaging	modulepath		Command	
Interactive	andanth		Command	
Interfaces	padpath		Command	
C Logic	parampath		Command	
Manufacture	psmpath		Command	
	step_facet_path		Command	
Os	step_mapping_path		Command	
🗀 Paths	steppath		Command	
🗀 Config	steppen		communa	
Editor	techpath		Command	
Library	topology_template_path		Command	
Signoise		ste	ppath Items	×
Placement		Directo	rice:	× • •
Change	bbΔ	Directo		<u> </u>
	Auu -	D:/Lil	orary/Step_Files	
rch for preference:	Directo	ry .		
Search		/ste	þ	
Include summary in search		D:/Lo	cal_Cadence/pcb/step	
		D:/W	orking/cdssetup/OrCAD_C	apture/17.4.0/d
mmary description				
tegory: paths/library				
arch path for STEP files (.stp .step). eppath = D:/Library/Step_Files . step .	/step D:/Local_Cadence/pcb/step D	:/Working		
egropcb		<		>
		Exp	oand OK	Cancel
OK Cancel	Apply	List A	I Info.	.:: He
Guidel	1,140	LISCA	Inom	

STEP Model Library

STEP models should be placed into a Library in a similar manner as other PCB Library models. A specific directory that all users have at a minimum read access permission should be defined. This path location is then assigned as a value to the PCB Editor Library path environment steppath. The steppath environment variable may be defined through Setup > User Preferences > Paths > Library setting in the PCB Editor.

_

Mapping a STEP model to a symbol

This can be completed directly in a board file, package or mechanical symbol. It is recommended that this process is done in a symbol since every time you use that footprint the step model with already be mapped.

There are two supported 3d canvas methods at the time of writing this application note. You can either invoke the original canvas from Display > 3D Canvas or this icon. The mapping method although graphically different in principle is the same so won't be covered in this note. The preferred method would be to use Display > 3DX Canvas or this icon.

The example below goes through mapping directly in a board file.

Open PCB Editor then go to Display > 3DX Canvas, the 3DX Canvas opens showing the board.

To change the view, roll the mouse wheel to Zoom, control + middle mouse click to Pan and shift + middle mouse click to Rotate.

The next step is to look for the Model Mapper. This is usually shown as a tab on the right-hand side of the screen. If it is not, then choose View > Model Mapper.

Model Mapper		- e ×
Refresh 3D Models	Import	Export
Footprint	Device Mecha	nical
Search		
FOOTPRINT	3D MODEL	
2-SMD 48QFN_7X7 CAP0603 DF37N8-24DS-0 IND0805 MEM2061 PTS815-SIM-250-SMTR-LFS RES0603 S28-PH-K SAMTEC_TSW-102-07-G-D-LL SON 102-07-G-D-LL	XTAL_CCSV-T1A_MCX.step QFN4&_7SQXP53P1PAD.step 0603_STEP DF37N8-24D5-0.4V.stp 0805_STEP MEM2061-01-188-00-A.stp PT58155JM2505MTRLFS.stp 0805_STEP 0805_STEP 282-PH-K-SSTEP TSW-102-07-G-D-LLsp UFD_701_CDFN_ep	
SOT-23-3	CCD_Iail_OrCCIV.Stp	
SOT-23-5 SOT-23-6 USB4085-GF-A	SOT23-5.STEP SOT23-6L.stp USB4085-GF-A.stp	

The four columns in the model mapper are explained as follows:-

DECODOE						
KESU8US		COD STEP				
SAMTEC TSW-102	2-07-G-D-U	32D3TEP				
SOD-123EL	2-07-0-0-22	LED str				
SOT-23-3		SOT23.STEP		0	ŵ	
SOT-23-5		SOT2STEP				
SOT-23-6	Refresh 3D	model from	dis	k		
USB4085-GF-A		USB4stp				

- 1. Browse for step model.
- 2. Refresh step model from step path.
- 3. Delete current step model.
- 4. Override model colour In some instances models will be a default colour, this option allows you to choose a different colour.

To map a 3D STEP model in a board file, select the part in the model mapper – in this example SIOT23-3 is chosen. Select the ... icon in the first column to browse for an appropriate step model. The list shows items in the steppath locations. Find the step model required, select then click OK. In this example a SOT23-3.stp is chosen.

			—	\circ \times
				cāden
		Model Mapper		
		Refresh 3D Models	Import	Export
		Examples	Davier	and and
		Footprint	Device Mech	anical
		Search	T	
		FOOTPRINT	3D MODEL	
		2-SMD 48QFN_7X7 CAP0603 DF37NB-24DS-0	XTAL_CC5V-T1A_MCX.step QFN48_7SQXP5PX1MMH_3P1PAD.step 0603.STEP DF37NB-24DS-0.4V.stp	
Select 3D model from STEPPATH	? X	IND0805	0805.STEP	
Search		PTS815-SJM-250-SMTR-LFS	PTS815SJM250SMTRLFS.stp	
		RES0603	0603.STEP	
Name	Size	RES0805	0805.STEP	
002 6141 C 1410190 02 2 STEP	1565 0 KD	S2B-PH-K	S2B-PH-K-S.STEP	
002 6142 C 1410182 02 2 STEP	1303.0 KB	SAMTEC_TSW-102-07-G-D-LL	TSW-102-07-G-D-LL.stp	
0005-0142 C-1410187-05-5.51EF	2992.0 KB	SOD-123FL	LED_Tall_GREEN.stp	
011-7441 Multi-Gig Guide Module 09-07-19 STEP	1107.0 KB	SOI-23-3		· · · ·
0201 step	132.2 KB	SOI-23-5	SOI23-5.STEP	
022-6057 ACD-105995-01 CTED	9946 3 KB	SOI-23-6	SOI23-6L.stp	
023-6059 LSH-030-01-G-D-A etc	20945.9 KP	USB4085-GF-A	USB4085-GF-A.stp	
023-6080 EX128-40P-0.45V stp	9757.2 KP			
0402 STEP	115 4 KP			
0402 AVX step	30.7 KB			
0476420001 stn	120.2 KB			
0603 STEP	73 9 KB			
0805 STEP	575 9 KB			
1002-902 15kV TX STEP	1040.4 KB			
10118194 STEP	2292 3 KB			
10118241-001rlf asm STEP	3437.1 KB			
1206 STEP	584 3 KB			
1206SMD step	584 3 KB			
1210.STEP	137.5 KB			
1210 AVX.step	30.7 KB			
1218.step	56.6 KB			
1812.step	512.2 KB			
2010.step	868.2 KB			
2220.STEP	206.1 KB			
2512.step	526,9 KB			
2520.step	663.5 KB			
2525.STEP	84.9 KB			
30X30X10 DC fan.step	779.5 KB		l	
Browse files	k Cancel			

Once selected the model is shown in the main canvas, it will be easier to align by choosing Device Only (to only show one part).



The part may or may not be aligned correctly with the footprint based on how the step model coordinate system was defined. In this example it is not:-



© 2025 Parallel Systems Limited



When the model is mapped the orientation is not correct as shown above. For this example we will use TOP initially to get the connector bottom align#ed with the top face of the PCB. Choose TOP then using the SHIFT + Middle mouse button, spin the model around until the correct face of the connector you want to align with the top face of the PCB is selectable, hover your mouse over the different faces (they will highlight blue), once you are happy, left click the face and the connector face selected is aligned with the top face of the PCB.

Using STEP file capability in PCB Editor





Next spin the board around so you can see the pins and holes:-



Now choose XY then left-click two pins the connector then choose two holes (that match the order of the pins), then right-click - Snap



© 2025 Parallel Systems Limited

The connector is now aligned with the holes.



The next option for positioning a step model is to use the MAN (manual) option. Select the required step model and the default position isn't correct. Using AUTO doesn't help too much either.





I'll start by using TOP to align the back face of the transistor with the top face of the PCB as we did earlier (so invoke TOP then left-click the back face of the part. This is looking better but the orientation of the part isn't correct.



© 2025 Parallel Systems Limited

Using the rotation buttons as shown select each one to rotate the step model into the correct orientation.



Once the transistor has been rotated correctly use the MAN button to adjust the exact position.



Drag the coloured arrows to position the transistor correctly on the pad. You can also manually type values in the X, Y and Z boxes to fine tune the exact position which are as follows:-

		AUTO		
MAN	TOP	BTM	XY	Z
	Offset		Rotation	
X 2.500		X 90.	0	• ©
Y 0.000		Y 0.0		▼ C [*]
Z 2.600		Z 270	0.0	• ©

You can export all the mapping information into an xml-based file. Use the Export button at the top of the Model Mapper window to export this data out. You can also import this mapping using the import button.

Model Mapper		-	8	×
Refresh 3D Models	Import	Exp	oort	

Back in the main canvas window (2d) you can invoke the Show Element form, the STEP model mapping properties are displayed. These properties become part of the symbol definition and cannot be modified outside of the STEP 3d Mapper. You can see all X, Y and Z rotation and offsets are stored as part of the package symbol.

Using STEP file capability in PCB Editor



You can also open the relevant Package Symbol file (filename.dra) and repeat the steps above to map STEP Models directly to the library of package and mechanical symbols. Once complete, any drawing that loads this symbol will also have the mapped data included. This is the recommended method.

3DX Canvas with STEP models

Once STEP model mapping is completed, the PCB Editor 3D Canvas will display the graphical representations of the STEP models.

Select the 3DX viewer icon in the PCB Editor toolbar 3D, or from the menu select Display > 3DX Canvas.

When the 3DX canvas opens, the symbols (with mapping associated) are now replaced by STEP models. Rotating and panning through the 3D image, placement of devices can be viewed for possible issues in the design.



This design has mechanical models (top and bottom covers and a dimm module PCB that is mated into the connector on the back side of the PCB). The Cadence Constraint Manager has a 3D constraint section where rules



for the 3D objects can be defined. You can create classes for the different packages or pick a specific component to check to, like the cover for example. For further details on rule creation refer to the Constraint Manager User Guide.

The Visibility pane controls what is displayed and allows all objects to have their colours defined. This includes 3d bending for flexis.

Boundary Place Bound DFA	Bound I	Both ▼
• Top (145)		
O Bottom (210)		
' 💽 Mechanical (5)	MDL	0
SHIELD2		0
SLOT_2WAY		0
STEP3D_MECH_BTMCOVER	XX	0
STEP3D_MECH_DIMM	XX	0
STEP3D_MECH_TOPCOVER	**	0
		© 2025 I

Visibilit	У			×
\$ <mark>.</mark>				
▼ Gen	eral			
Project	ion	Perspecti	ve Ort	hographic
				Plate Closed
Microv	/ias			
Blind/E	Buried Via	s		
Mate	erial Color	'S		
Viev	v Colors			
Sele	ction			
DRC	Display			
Bend	ding			
Select	Name	Order	Percentage	Angle
\checkmark	BEND_1	0		180.0 🔺 🔻
\checkmark	BEND_2	0		180.0 🔺 🔻
\checkmark	BEND_3	0		180.0 🔺 🔻
\checkmark	BEND 4	0		200.0 🔺 🔻
\checkmark	BEND 5	0		450▲▼
		0		
\checkmark	BEND_6	U		180.0
	All	▼		
• 💋	Cutting P	lanes		+
ed				Р



Mapping Mechanical STEP Models

Viewing STEP models in the PCB Editor 3DX Canvas provides a more accurate representation of on-board symbols. The user can view component placement and visually determine (or generate a Constraint rule) one component's proximity to another to decide if a violation has occurred. The ability to view other objects, such as shields and housings is possible if those objects are represented by STEP models. PCB Editor STEP model support provides the ability to map these other types of objects. Once mapped to the board design, the 3DX Canvas will display these models where the user can then run a DRC check to look for any DRC errors much the same way as a physical or spacing rule would work.

For mechanical parts that are placed on a board (using a mechanical symbol) are shown in the Visibility pane component tabs by default. You can add the step model to the actual mechanical symbol the same way you would to a package symbol. This is also where you would add parts manually for example a cover or a heatsink that doesn't have a mechanical symbol.

Launch the 3DX Canvas then in the Visibility tab click on the Components tab. You will see the Mechanical dropdown listing the mechanical parts. To add a new one, click on the + icon.

Visibility		_ & X
Boundary Place Bound	DFA Bound	Both
• • • Top (46)		
 Bottom (0) Mechanical (1) 		+
	MDL	•
SHIELD3	**	0

|--|

Select 3D model from STEPPATH		?	×
Search			
Name			Size
003-6141 C-1410189-03-3.STEP		1565	.8 KB
003-6142 C-1410187-03-3.STEP		2992	.8 KB
009652068.stp		161	.5 KB
011-7441 Multi-Gig Guide Module_08-07-19.STEP		1197	.9 KB
0201.step		123	.3 KB
023-6057 ASP-105885-01.STEP		8846	.2 KB
023-6059 LSH-030-01-G-D-A.stp		20845	.8 KB
023-6080 FX12B-40P-0.4SV.stp		8757	.2 KB
0402.STEP		115	.4 KB
0402_AVX.step		30	.7 KB
0476420001.stp		120	.2 KB
Browse files	Ok	Ca	ncel

You can now browse the steppath for the required mechanical symbol, you also have the option to browse a different directory.

Once you click OK the step model is added to the mechanical list and is also placed at the design origin location.





Using the method you used to position package symbols, you would the adjust the position of the step model in the model mapper.



To delete a mechanical part right-click the mechanical part name in the visibility pane and choose Delete.

Exporting a Board to a STEP Model.

STEP model export supports AP203, AP204 and AP242 protocols, standard units, and various output options to minimize or maximize STEP model data. **Potential for very large STEP model files exists when exporting STEP Model Parts and external copper data.**

From the 3DX Canvas look for the 3DExport tab. If this is not shown use View > 3DExport to display the tab.

DExport	P
3D evport data based on	
Options 3	D Display
Components	
Mechanical	
2D Models	
Components without 3D mode	ale 📃
Place Bound	DFA Bound
lavers	
Die stack conductors	
External conductors	
Internal conductors	
Dielectrics	
Soldermask	
Soldernaste	
Silkscroop	
Holes	
Mechanical	
Through hole pins	
Vias	
Bond wires	
Flex in fully bent state	
Output Units	
mm cm um ir	nches mils
Output Type	
STEP	•
STEP Protocol	
AP214	•
7 = 0 aver	•
BOTTOM	•
Z = 0 Layer Position	
LOWER	•
Separate file per layer	
Compress output file (.zip)	
Canc	el Export

3DExport	_ & ×
3D export data based on	
Options	3D Display
Set the 3D display to represent the	e desired 3D export results
Selected objects only	
Output Units	
mm cm um	inches mils
Output Type	
STEP	▼
STEP Protocol	
AP214	▼
Z = 0 Stackup	
PRIMARY	▼
Z = 0 Layer	
BOTTOM	▼
Z = 0 Layer Position	
LOWER	▼
Separate file per layer	
Compress output file (.zip)	

Choose the required settings and export the 3d Step Model. You can also invoke this command from with PCB Editor File > Export – 3D (Allegro) and Export > MCAD > 3D (OrCAD). This shows the same menu options as above.

The following are trademarks or registered trademarks of Cadence Design Systems, Inc. 555 River Oaks Parkway, San Jose, CA 95134 Allegro®, Cadence®, Cadence logo™, Concept®, NC-Verilog®, OrCAD®, PSpice®, SPECCTRA®, Verilog® **Other Trademarks**

All other trademarks are the exclusive property of their prospective owners.

NOTICE OF DISCLAIMER: Parallel Systems is providing this design, code, or information "as is." By providing the design, code, or information as one possible implementation of this feature, application, or standard, Parallel Systems makes no representation that this implementation is free from any claims of infringement. You are responsible for obtaining any rights you may require for your implementation. Parallel Systems expressly disclaims any warranty whatsoever with respect to the adequacy of the implementation, including but not limited to any warranties or representations that this implementation is free from claims of infringement and any implied warranties of merchantability or fitness for a particular purpose.